

REMARKS

Please reconsider the present application in view of the above amendments and the following remarks. Applicant thanks the Examiner for carefully considering the present application and indicating that claims 8, 19, and 30 contain allowable subject matter.

I. Disposition of Claims

Claims 1-33 are pending in the present application. Claims 1, 8, 12, and 23 have been amended.

II. Claim Amendments

Independent claims 1, 12, and 23 have been amended to incorporate the limitation that noise of the representative power supply waveform is that which is incident to supplying power to the phase locked loop. No new matter has been added by way of these amendments as support for these amendments may be found, for example, in paragraphs [0025] and [0026] of the Specification.

Dependent claim 8 has been amended to incorporate the limitations of original claims 1 and 7. No new matter has been added.

III. Rejection(s) under 35 U.S.C § 102

Claims 1-7, 9-18, 20-29, and 31-33 were rejected under 35 U.S.C. § 102(b) as being anticipated by Jenkins et al. ("Measuring Jitter and Phase Error in Microprocessor Phase-Locked Loops," Keith A. Jenkins and James P. Eckhardt, IEEE Design and Test of

Computers, April – June 2000, pages 86-93). For the reasons set forth below, this rejection is respectively traversed.

The present invention is directed to a technique for estimating/determining jitter of a phase locked loop based on a simulation of the phase locked loop using a representative power supply waveform. Specification, paragraph [0025]. Independent claims 1, 12, and 23 of the present application require at least that (1) a representative power supply waveform having noise be inputted for simulation of the phase locked loop and (2) jitter be estimated based on the simulation. As described in the present application, the noise of the representative power supply waveform are deviations from a desired voltage of a power supply of which the representative power supply waveform is representative. *Id.* Accordingly, amended independent claims 1, 12, and 23 additionally require that the noise of the representative power supply waveform be incident to the act or simulation of supplying power to the phase locked loop. *Id.*

Contrastingly, Jenkins et al. is directed to a technique for estimating jitter of a phase locked loop by “*creat[ing] a voltage change (noise) on the power supply.*” Jenkins et al., page 88, second column, last paragraph (emphasis added). To this end, Jenkins et al. uses a specific noise generator circuit, as shown in Figure 5 of Jenkins et al., to create voltage steps and/or short noise pulses. Jenkins et al., page 89, column 1, first paragraph. In other words, the noise in the Jenkins et al. simulation is not incident to the supply of power to the phase locked loop, but is instead specifically created for simulation purposes. Even if the noise generator of Jenkins et al. is accurate enough to produce noise pulses that emulate the noise found in a power supply system, such noise pulses are not incident to the power supply system because they are specifically generated for

simulation purposes. The present invention, on the other hand, inputs a power supply waveform that is representative of a power supply having noise, where the noise is incident to the simulated supply of power to the phase locked loop. No particular noise pulse or behavior is deliberately generated. Instead, the noise of the representative power supply waveform is that which is parasitic, residual, or a non-intentional effect of the power supply in simulation. Accordingly, Jenkins et al. does not, either explicitly or inherently, disclose a simulation technique in which a representative power supply waveform having noise that is incident to supply power to a phase locked loop is used for simulation of the phase locked loop.

In view of the above, Jenkins et al. fails to show or suggest the present invention as recited in amended claims 1, 12, and 23. Thus, amended claims 1, 12, and 23 are patentable over Jenkins et al. Dependent claims are allowable for at least the same reasons. Accordingly, withdrawal of this rejection is respectfully requested.

IV. Allowable Subject Matter

The Examiner indicated that claim 8 would be allowable if rewritten in independent form including all of the limitations of any base and intervening claims. By way of this reply, claim 8 has been amended to incorporate the limitations of original claims 1 and 7, on which claim 8 depends. Accordingly, allowance of claim 8 is respectfully requested.

V. Conclusion

Applicant believes this reply to be fully responsive to all outstanding issues and

place this application in condition for allowance. If this belief is incorrect, or other issues arise, do not hesitate to contact the undersigned or his associates at the telephone number listed below. Please apply any charges not covered, or any credits, to Deposit Account 50-0591 (Reference Number 03226.170001/P7188).

Respectfully submitted,



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